

4/ Prel. Amdt.  
A  
E. Willis  
5-13-02

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Atty. Docket

JOHANN FUHRMANN ET AL

DE 010007

Serial No.

Filed: CONCURRENTLY

ELECTRIC OR ELECTRONIC CIRCUIT ARRANGEMENT AND METHOD OF  
PROTECTING SAID CIRCUIT ARRANGEMENT FROM MANIPULATION AND/OR  
ABUSE

Commissioner for Patents  
Washington, D.C. 20231

PRELIMINARY AMENDMENT

Sir:

Prior to calculation of the filing fee and examination,  
please amend the above-identified application as follows:

IN THE CLAIMS

Please amend the claims as follows:

A1  
3. A circuit arrangement (100) as claimed in claim 1,  
characterized in that the mutual distance (d) between the  
conductor tracks (20, 25) is in the micrometer range.

4. A circuit arrangement (100) as claimed in claim 1,  
characterized in that the material of the dielectric  
shielding layer (30; 35) is epoxy resin or silicon nitrite  
(SiNO<sub>2</sub>) or silicon dioxide (SiO<sub>2</sub>) or consists of other  
insulating layers used in the manufacture of semiconductors.